

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q93490

Kazuhiro IIDA, et al.

Appln. No.: 10/569,835

Group Art Unit: 1797

Confirmation No.: 2558

Examiner: Neil N TURK

Filed: February 28, 2006

For: CHIP

INFORMATION DISCLOSURE STATEMENT  
UNDER 37 C.F.R. §§ 1.97 and 1.98

MAIL STOP AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the documents which are listed on the attached PTO/SB/08 (modified) form and/or listed herein and which the Examiner may deem material to patentability of the claims of the above-identified application.

Applicants submit herewith a copy of a corresponding Japanese Office Action April 28, 2009 along with an English translation of the pertinent portions thereof.

All listed references cited therein<sup>1</sup> have been previously submitted to the USPTO in an Information Disclosure Statement filed February 28, 2006.

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<sup>1</sup> 1. JP 2001-502793 A, published February 27, 2001;

2. WO 02/23180 A1, published March 21, 2002;

3. JP 2003-222611 A, published August 8, 2003;

4. JP 11-352094 A, published December 24, 1999;

...(footnote continued)

One copy of each of the listed documents is submitted herewith, except for the following: U.S. patents and/or U.S. patent publications; and co-pending non-provisional U.S. applications filed after June 30, 2003.

The present Information Disclosure Statement is being filed: (1) No later than three months from the application's filing date; (2) Before the mailing date of the first Office Action on the merits (whichever is later); or (3) Before the mailing date of the first Office Action after filing a request for continued examination (RCE) under § 1.114, and therefore, no Statement under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

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5. JP 8-327594 A, published December 13, 1996;

6. JP 6-10900 A, published January 21, 1994;

7. M. WATANABE et al., "Fabrication of a Micromixer Chip with High-Aspect-Ration Microstructures", Extended Abstracts (The 63<sup>rd</sup> Autumn Meeting, 2002); The Japan Society of Applied Physics, 24 September 2002, p. 1147 (25a-R-11), No. 3, Japan;

8. T. SANO et al., "Proposal of novel nano-biochips in the Basis of Size Exclusion Chromatography and their DNA Separation" Extended Abstracts (The 63<sup>rd</sup> Autumn Meeting, 2002); The Japan Society of Applied Science, 24 September 2002, p. 1147 (25a-R-8), No. 3, Japan;

9. KAZUHIRO IIDA et al., "Rapid plasma extraction using planar ultra-filtration chip", Extended Abstracts (The 63<sup>rd</sup> Autumn Meeting, 2002); The Japan Society of Applied Science, 24 September 2002, p. 1147 (25a-R-9), No. 3, Japan;

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Attorney Docket No.: Q93490

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

Respectfully submitted,

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WASHINGTON OFFICE

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Date: May 27, 2009